Etch Endpoint detection of low open area contact by means of new CCD optical emission spectroscopy techniques developed with Jobin-Yvon.

CCD endpoint

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CCD endpoint



Dry etch is a critical step in semiconductors manufacturing. While new products and processes are continuously developed and improved, the technology becomes ever more complicated with the increase of the device density. End-point detection of low open areas presents a serious challenge for process monitoring.

In a joined effort, we developed a new generation of Multi Sensor Platform for fault detection, health monitoring and Advanced Process Control (APC). Based on innovative technologies like smart sensors, a unique software architecture including analytical methodology, and a sophisticated signal processing, this platform allows to satisfy all the needs of in-situ process control.

These slides describe the first results obtained for via etch process (open area from 1 to 10%) on leading edge products (0.18 μ m-0.13 μ m technology with copper backend) by using OES (optical emission spectroscopy) sensors as endpoint detection (EPD). All experiments were conducted on high density plasma oxide etcher tools and show excellent stability in high volume production.



CCD endpoint



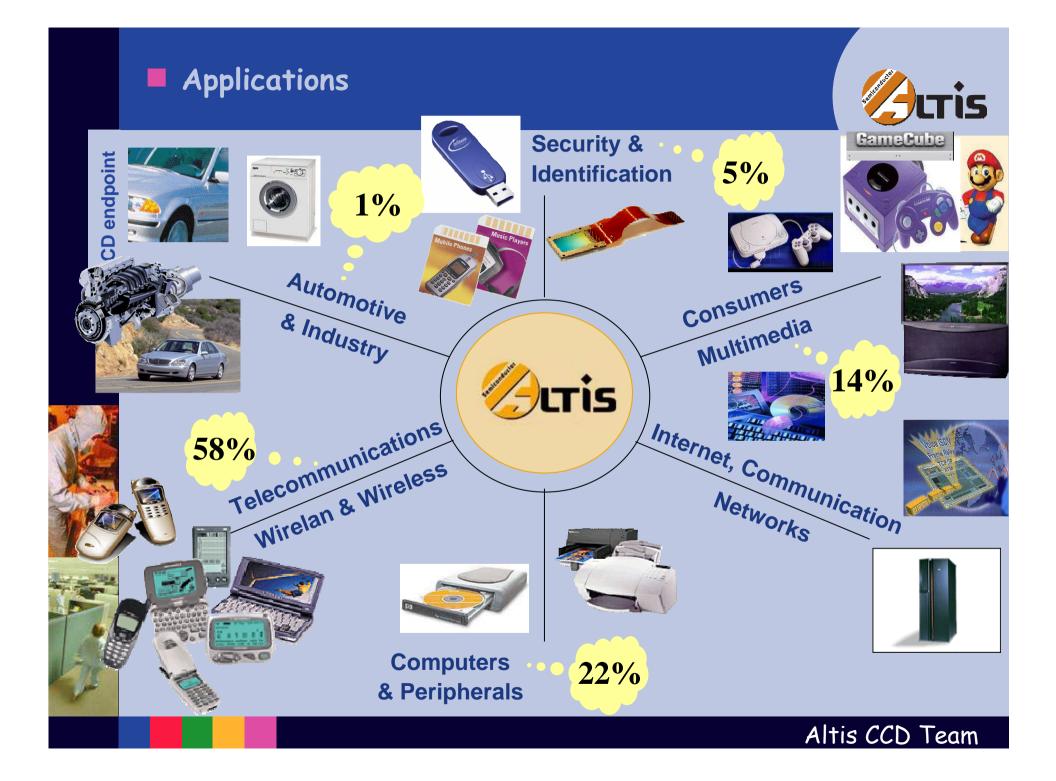
- ALTIS Semiconductor
- Multi sensor platform for APC (JY)
- Plasmascope used as Optical Emission Spectroscopy on Via etch
 - Step 1 : Algorithm development
 - Step 2 : Recipe and endpoint optimisation
 - Step 3 : Endpoint algorithm validation
 - Step 4 : Robustness evaluation
- Other application
- Conclusion

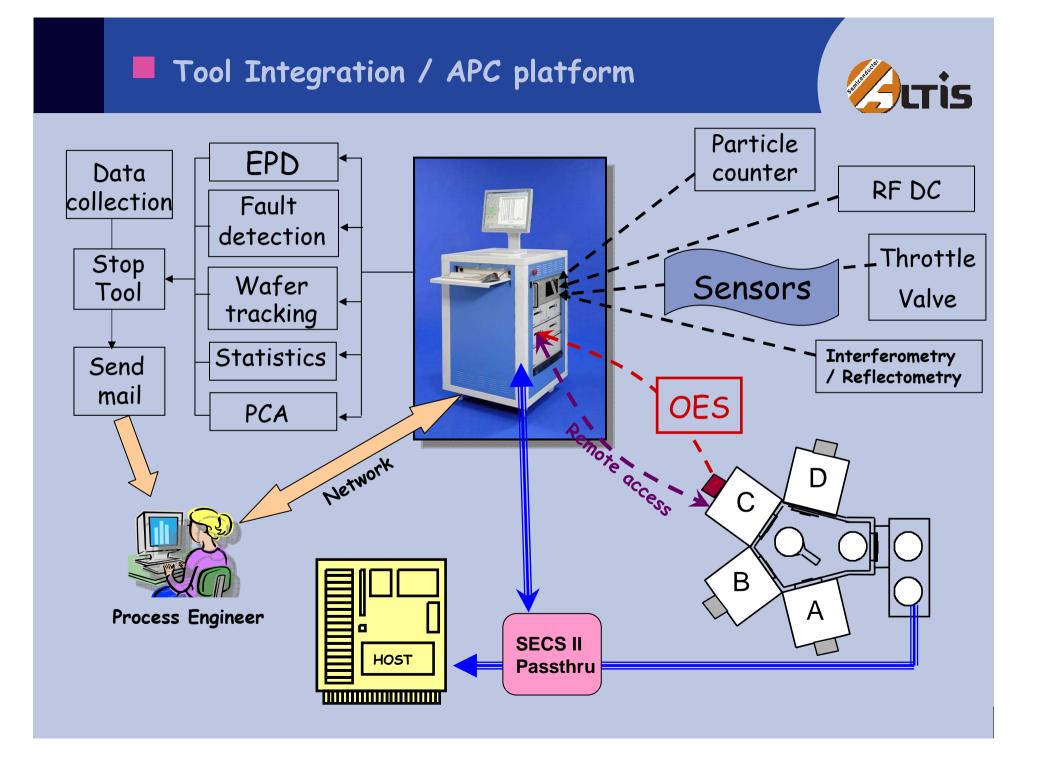


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Major key points

CCD endpoint

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- > OES capabilities for plasma spectral analysis
 - Integrated OES Library
 - Full reprocessing
 - Script, APC engineering
 - Advanced mathematical spectrum analysis for finding EPD participating wavelengths : AUTOPATTERN

Advanced ENDPOINT detection

- Multi-wavelengths acquisition, arithmetical operations
- Filtering
- New EPD algorithms
- Sensitivity improvement algorithms

Production control capabilities

- Full database (SQL)
- Statistics recipes
- Chamber health R2R control, fault detection, chamber health (AEC/APC management)

User Friendly settings

Quick recipe editor, LEGO block, help on line, one click action button

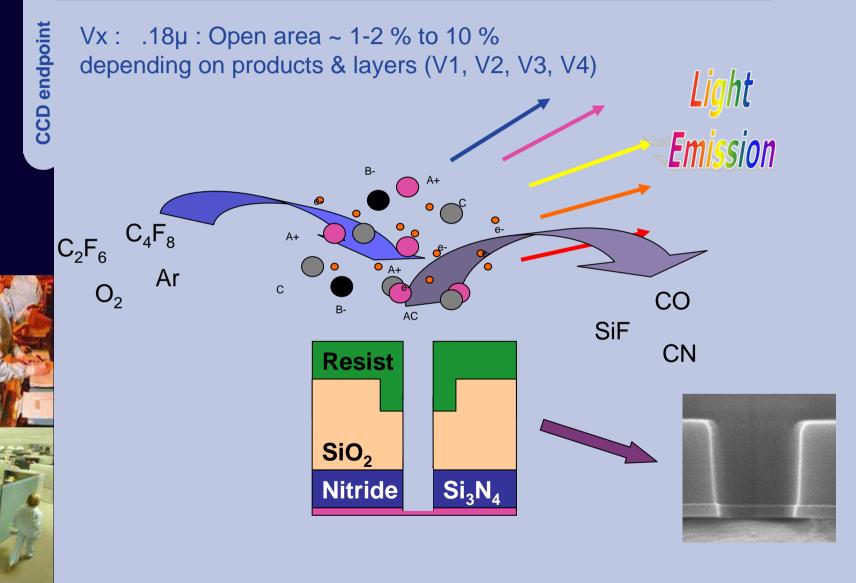
New interface capabilities

FAB communication through TCP/IP, SECS_Pass through, Email auto sender, platform for external tool sensors, distant access available (DB, recipes, reprocessing from Desktop)



Application example : Via etch on .18 μm technology on HDP tool

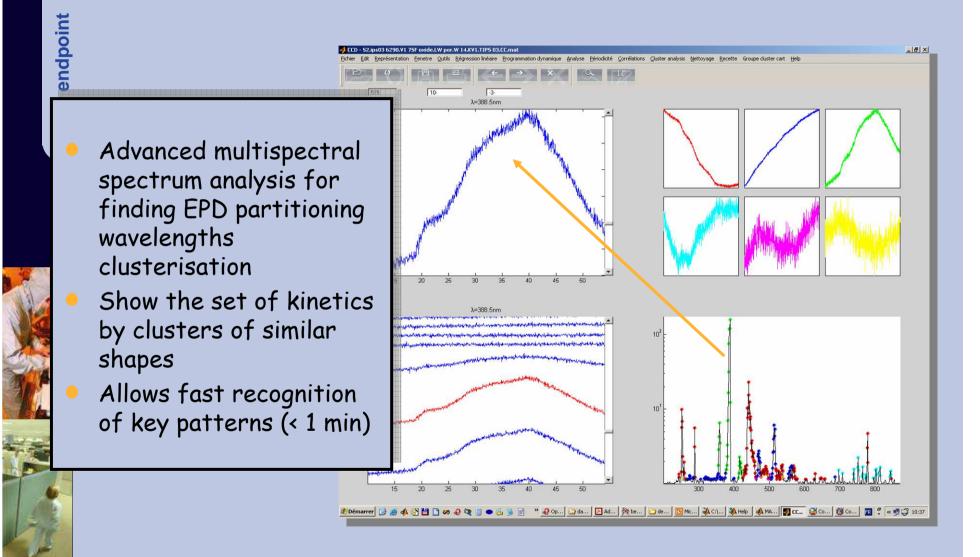






Step 1 : Algorithm development > Spectra and clusterisation analysis





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Step 1 : Algorithm development > Recipe build up

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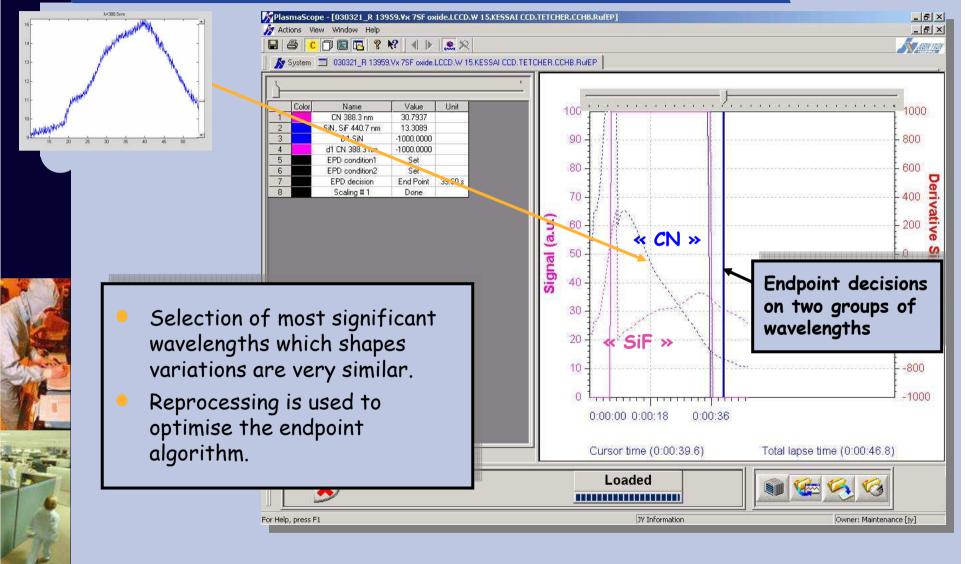


CCD endpoin	For	rnula Editor - Variables CN3 (Fine Gain)	-Functions And Then	× ?
	 The recipe editor allows to use mathematical, logical operations and advanced filters on the selected wavelengths >> high sensitivity algorithms 	Elements CN Add rmula Max (480, 560) +Av g (256, 272) +Sum (Sp (388.3) / Spec (440.7))And Then Check	All Wavelengths 358.6 359.0 386.2 387.1 388.3 ec (358.6) , Spec (386.2)) + (Spec OK Cancel	



Step 2 : Recipe and EPD optimisation



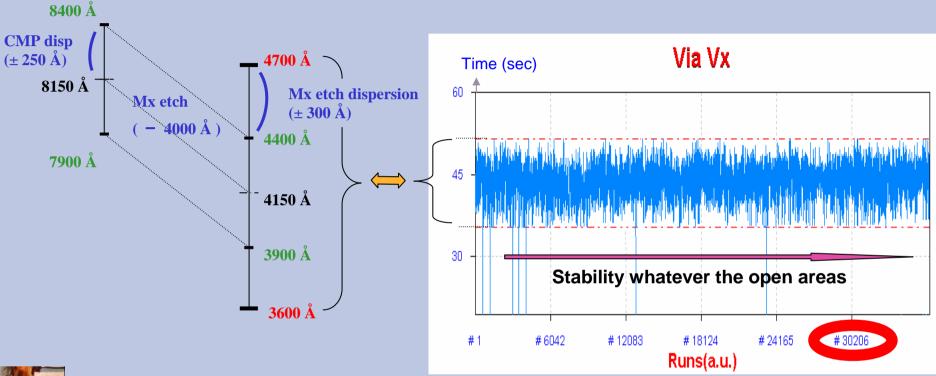




Step 3 : Endpoint algorithm validation (1)



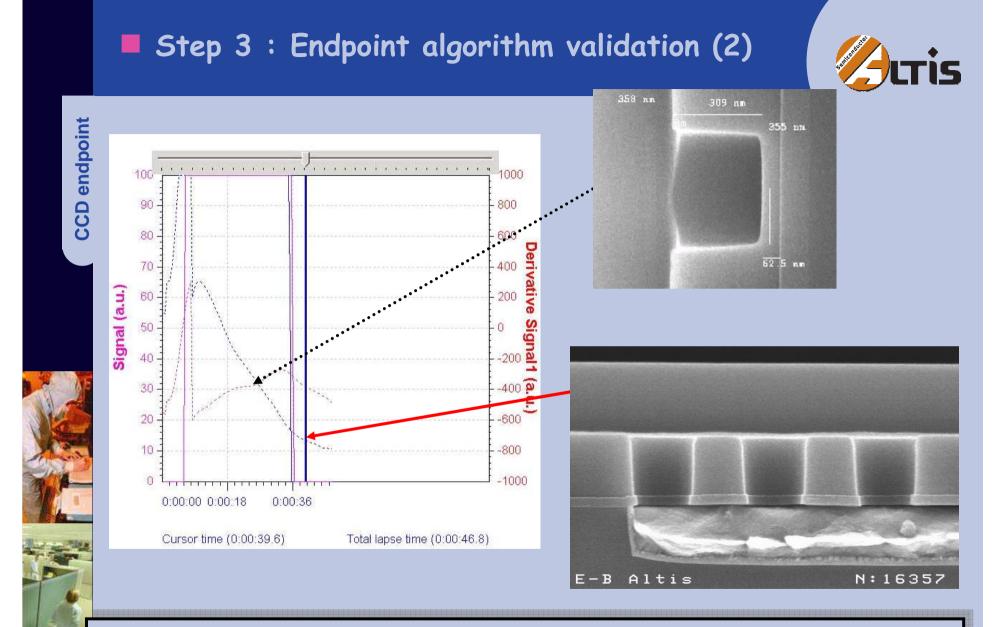
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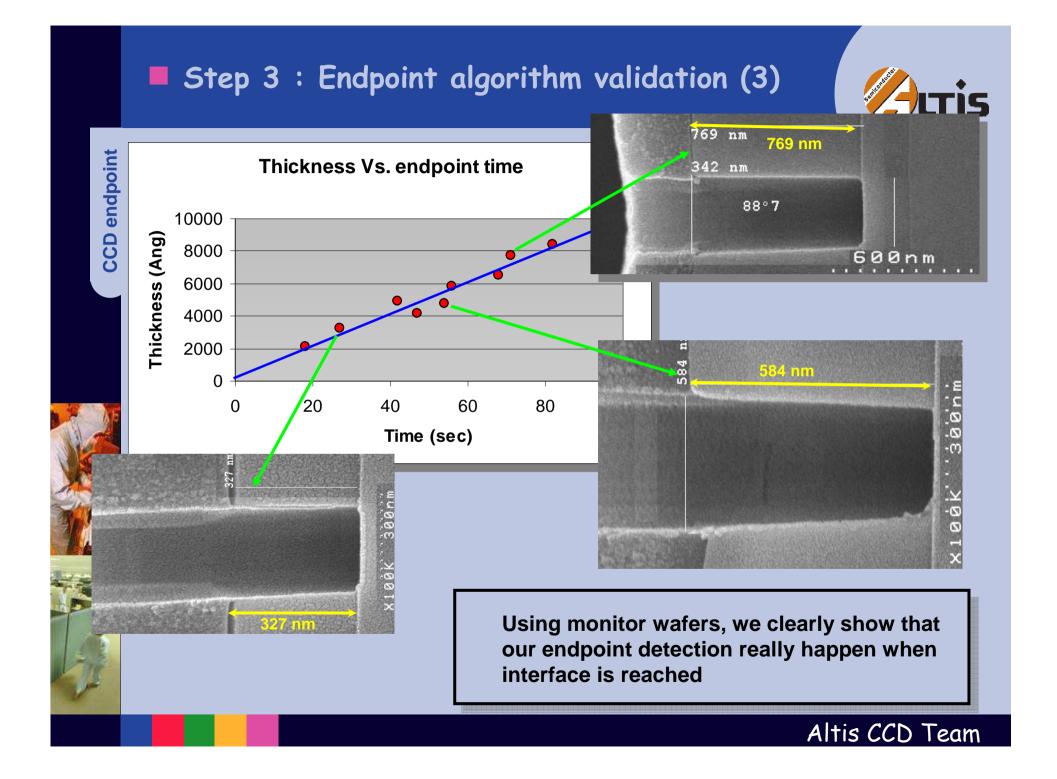
• CMP and Mx etches introduce oxide thickness variation (between 3600 and 4700 Å) which can explain an endpoint time variation of about 11 sec (if we consider an etch rate of 100 Å/sec)

- Moreover , we have to add the variation given by :
 - the effect of CMP and Mx non uniformity
 - the different open area (1 to 6%)



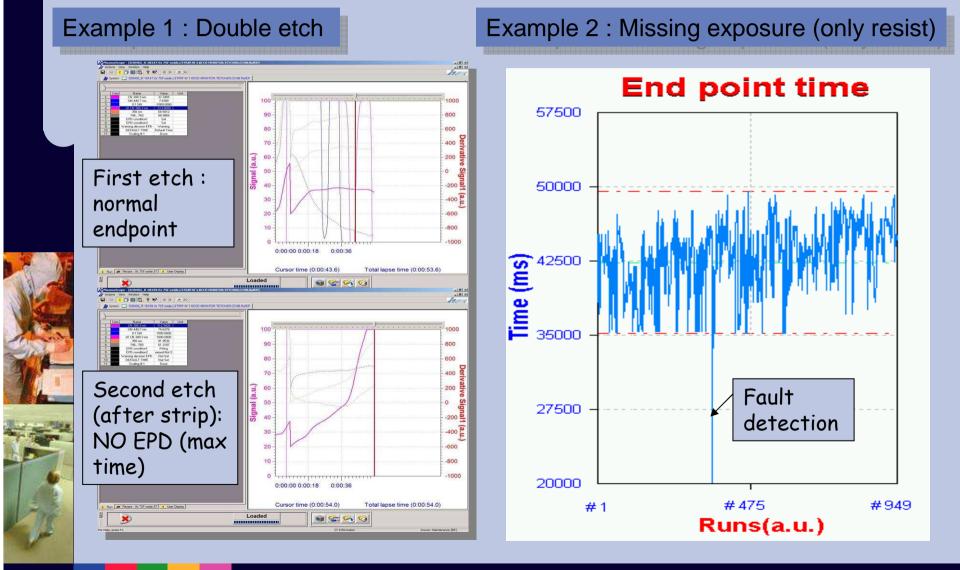
SEM pictures show that we reach the interface only when the endpoint decision is complete





Step 4 : Robustness evaluation

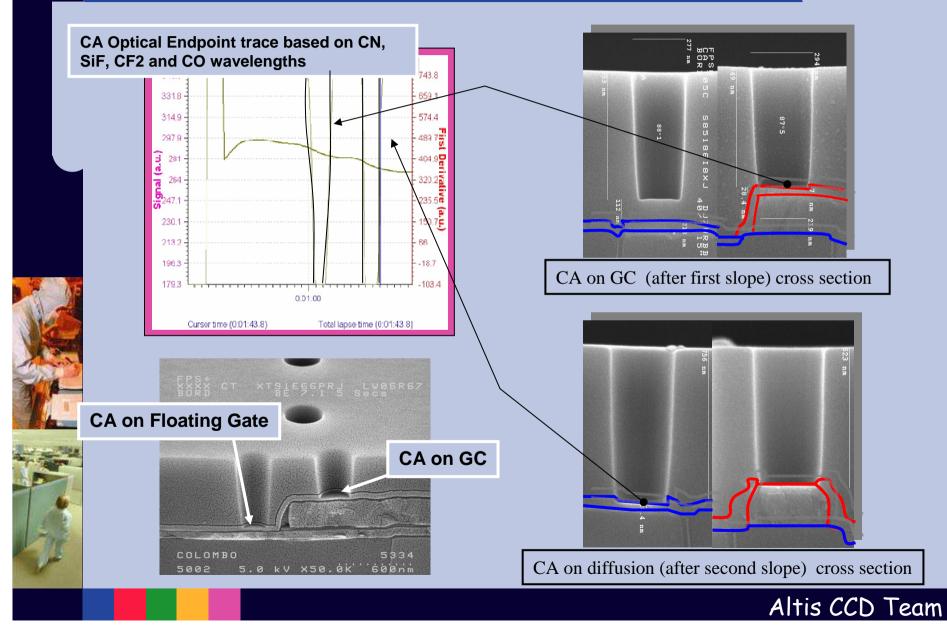




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Other application







Conclusion



- A new platform was designed and developed for critical endpoint detections on which traditional methods were unsuccessful.
- It includes health monitoring capabilities to reduce tools downtime, optimise quality and secure wafers production.
- This platform was successfully implemented on tools running Via and Contact etch processes.
- The ability to interface most of the current etchers from any vendor (AMAT, TEL, LAM) was also demonstrated.
- Further developments are on-going in order to implement interferometry applications and additional faults detection methods.

